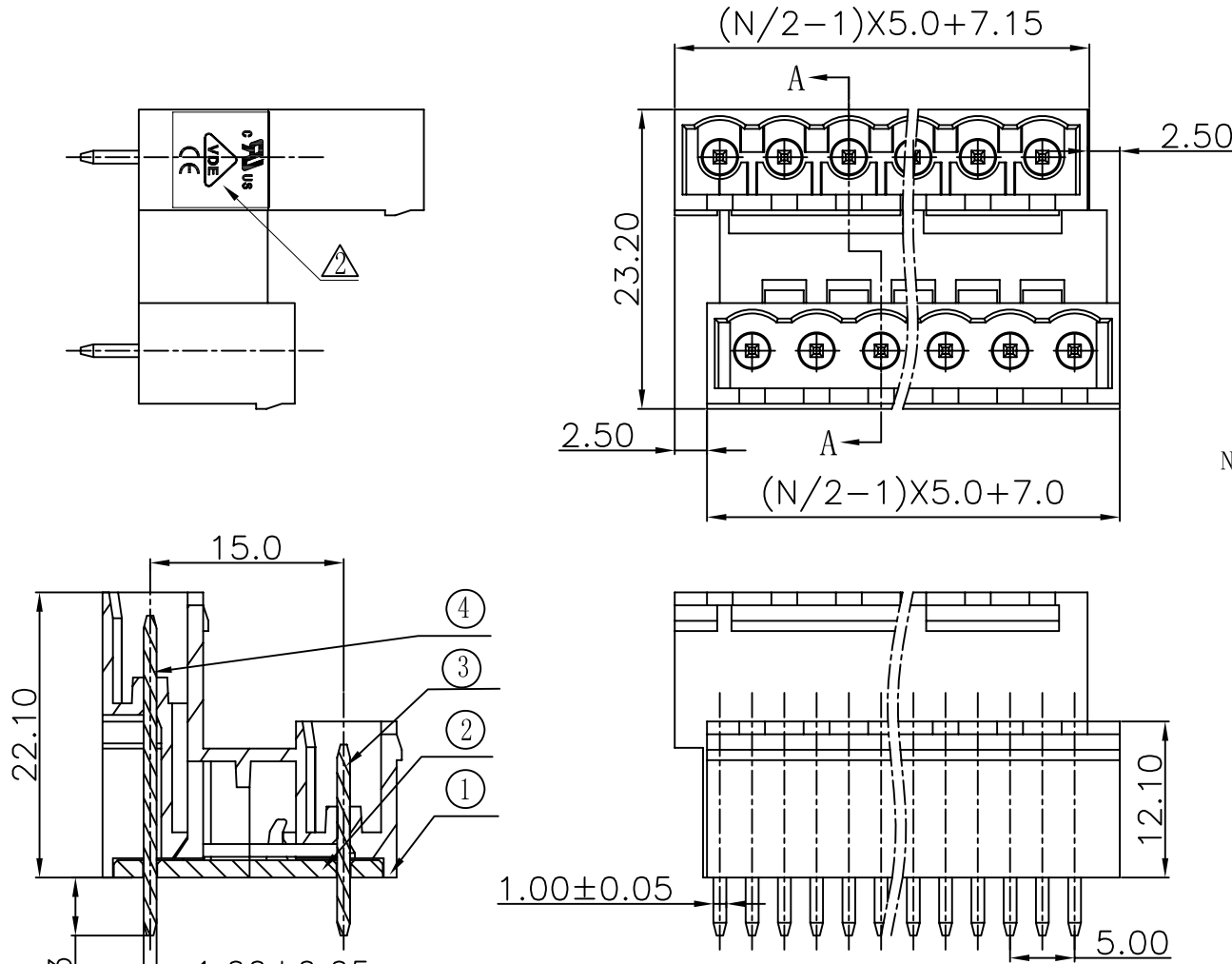


REVISION RECORD					
REV	ECO	DESCRIPTION	DRFT	DATE	
△		品名、料号增加环保标示“(H)”	LJH	12/11/07*	
△		增加VDE刻字	Zengy	11/12/08*	



Notes:

1. Pitch:5.0mm , Poles: One-block for 4-24P
Inter locking for 24-48P
2. Rated voltage/current:UL 300V/15A
VDE 320V/10A
3. Operation temperatures:-40℃~+105℃
4. Withstanding Voltage:AC2000V/1Min
5. Insulation Resistance:5000MΩ /DC1000V
6. Undimensioned Tolerances:

Pitch range in mm		Nominal dimension range in mm							
to	over	to	over	to	over	to			
6	6	10	10	30	30	60	100	150	—
									∩
									◇
									±0.05/10

SECTION A-A

4	2EDGVT-long pin	1032B00010 (H)	XX/2	Brass	SnCe
3	2EDGV-pin	1007B00030 (H)	XX/2	Brass	SnCe
2	2EDGVT-5.0-Cover	1032COY (XX/2 & XX/2 +1) A (H)	1	PA66	>24P
	2EDGVT-5.0-Cover	1032COY (XX/2 & XX/2 -1) A (H)	1	PA66	
	2EDGVT-5.0 Cover	1032COYXXA (H)	1	PA66	≤24P

1	2EDGVT-5.0 housing (Right foot)	1072AOY (XX/2 & XX/2 +1) C (H)	1	PA66	>24P
	2EDGVT-5.0 housing (Left foot)	1072AOY (XX/2 & XX/2 -1) B (H)	1	PA66	
		2EDGVT-5.0-Housing	1072AOYXXA (H)	1	PA66
ITEM	NAME OF PART	NO. OF PART	Q'TY	MATERIAL	NOTES

DETACHED LISTS	MM (INCH)	DFTO Apple	DATE 06/21/06*	宁波高正电子有限公司 DEGSON ELECTRONICS CO., LTD		
	TOLERANCES EXCEPT AS NOTED	CHKD	DATE			
		MFO	DATE			
		APPVL	DATE			
	MM			NAME		
	.0 ±	±		2EDGVT-5.0-XXP-1Y-00A (H)		
	.00 ±	±				
	.000 ±	±				
	ANGLES ± 1°	G.E :		DRAWING NO. 2EDGVT-5.0-E01	SIZE A3	REV T2
	THIRD ANGLE PROJECTION	FINISH :		/PART NO.	11/12/08*	
		SCALE :		DO NOT SCALE DRAWING	SHEET 1	OF 1