

HSM88WK

Silicon Schottky Barrier Diode for Balanced Mixer

HITACHI

ADE-208-0489F (Z)

Rev 6

Jul 1998

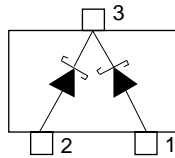
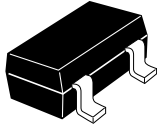
Features

- Proof against high voltage.
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HSM88WK	C4	MPAK

Outline



(Top View)

- 1 Anode
- 2 Anode
- 3 Cathode

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	10	V
Average rectified current	I_O^{*1}	15	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes 1. Per one device

Electrical Characteristics (Ta = 25°C) *1

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	350	—	420	mV	$I_F = 1 \text{ mA}$
	V_{F2}	500	—	580		$I_F = 10 \text{ mA}$
Reverse current	I_{R1}	—	—	0.2	μA	$V_R = 2\text{V}$
	I_{R2}	—	—	10		$V_R = 10\text{V}$
Capacitance	C	—	—	0.85	pF	$V_R = 0\text{V}$, $f = 1 \text{ MHz}$
Capacitance deviation	ΔC	—	—	0.10	pF	$V_R = 0\text{V}$, $f = 1 \text{ MHz}$
Forward voltage deviation	ΔV_F	—	—	10	mV	$I_F = 10 \text{ mA}$
ESD-Capability ^{*2}	—	30	—	—	V	C=200pF, Both forward and reverse direction 1 pulse.

Notes 1. Per one device

Notes 2. Failure criterion ; $I_R \geq 400\text{nA}$ at $V_R = 2 \text{ V}$

Main Characteristic

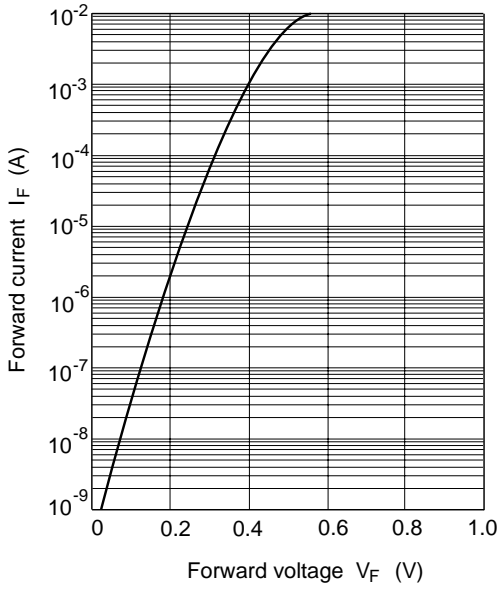


Fig.1 Forward current Vs. Forward voltage

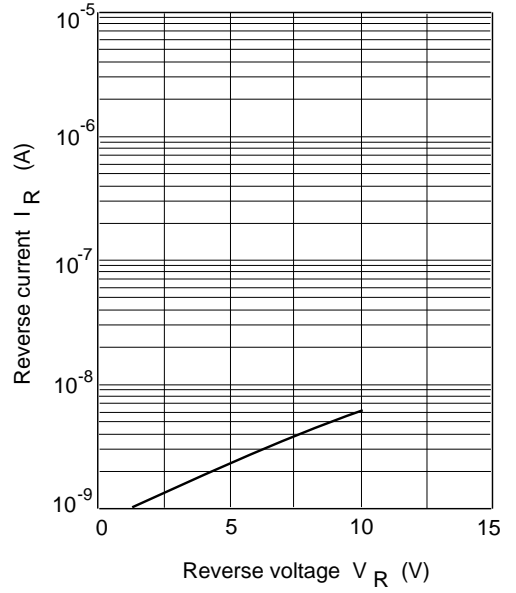


Fig.2 Reverse current Vs. Reverse voltage

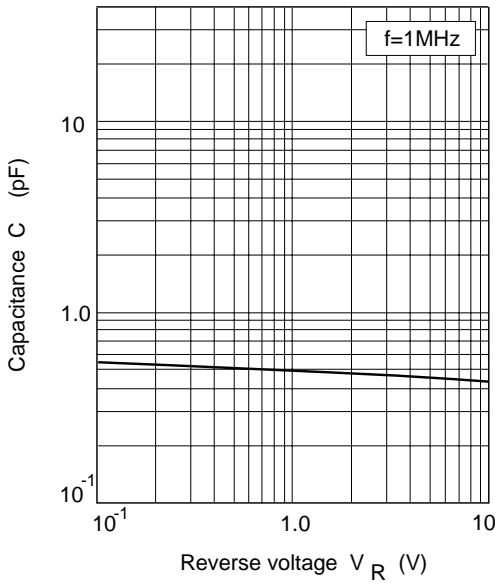
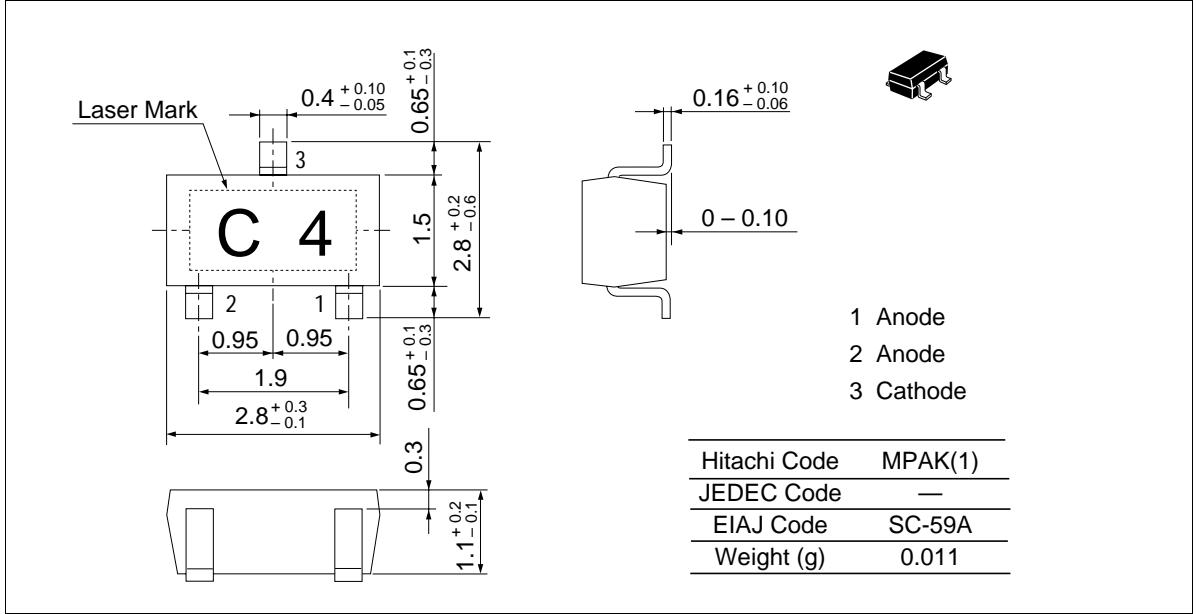


Fig.3 Capacitance Vs. Reverse voltage

Package Dimensions

Unit : mm



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Hitachi, Ltd.

Semiconductor & Integrated Circuits.
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL North America : <http://semiconductor.hitachi.com/>
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For further information write to:

Hitachi Semiconductor
(America) Inc.
179 East Tasman Drive,
San Jose, CA 95134
Tel: <1> (408) 433-1990
Fax: <1>(408) 433-0223

Hitachi Europe GmbH
Electronic components Group
Dornacher Straße 3
D-85622 Feldkirchen, Munich
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.
Electronic Components Group.
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA, United Kingdom
Tel: <44> (1628) 585000
Fax: <44> (1628) 778322

Hitachi Asia Pte. Ltd.
16 Collyer Quay #20-00
Hitachi Tower
Singapore 049318
Tel: 535-2100
Fax: 535-1533

Hitachi Asia Ltd.
Taipei Branch Office
3F, Hung Kuo Building, No.167,
Tun-Hwa North Road, Taipei (105)
Tel: <886> (2) 2718-3666
Fax: <886> (2) 2718-8180

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower, World Finance Centre,
Harbour City, Canton Road, Tsim Sha Tsui,
Kowloon, Hong Kong
Tel: <852> (2) 735 9218
Fax: <852> (2) 730 0281
Telex: 40815 HITEC HX

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