HD74HC182

Look-Ahead Carry Generator

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Description

The HD74HC182 is a high-speed Carry Lockahead Generator. It is used with the HD74HC181 4-Bit Arithmetic Logic Unit to provide high-speed lockahead over World lengths of more than four bits. The device accepts up to four pairs of active-low Carry Propagate $(\overline{P}_0, \overline{P}_1, \overline{P}_2, \overline{P}_3)$ and Carry Generate $(\overline{G}_0, \overline{G}_1, \overline{G}_2, \overline{G}_3)$ signals and an active-high carries $(C_{n+x}, C_{n+y}, C_{n+z})$ across four groups of binary adders. The HD74HC182 also has active-low Carry Propagate (\overline{P}) and Carry Generate (\overline{G}) outputs which may be used for further levels of lockahead.

The logic equations provided at the outputs are:

$$\begin{split} \overline{C_{n+x}} &= \overline{Y_0 \ (X_0 + C_n)} \\ \overline{C_{n+y}} &= \overline{Y_1 \ \{X_1 + Y_0 \ (X_0 + C_n)\}} \\ \overline{C_{n+z}} &= \overline{Y_2 \ [X_2 + Y_1 \ \{X_1 + Y_0 \ (X_0 + C_n)\}}] \\ Y &= Y_3 \ (X_3 + Y_2) \ (X_3 + X_2 + Y_1) \ (X_3 + X_2 + X_1 + Y_0) \\ X &= X_3 + X_2 + X_1 + X_0 \\ \text{or} \\ C_{n+x} &= G_0 + P_0 C_n \\ C_{n+y} &= G_1 + P_1 G_0 + P_1 P_0 C_n \\ C_{n+y} &= G_2 + P_2 G_1 + P_2 P_1 G_0 + P_2 P_1 P_0 C_n \\ G &= \overline{G_3 + P_3 G_2 + P_3 P_2 G_1 + P_3 P_2 P_1 G_0} \end{split}$$

Also, the HD74HC182 can be used with binary ALUs in an active-low or active-high input operand mode. The connections to and from the ALU to the carry lookahead generator are identical in both cases.



 $\overline{P} = \overline{P_3 P_2 P_1 P_0}$

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Features

• High Speed Operation: t_{pd} (Pn to P) = 11 ns typ ($C_L = 50$ pF)

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2$ to 6 V

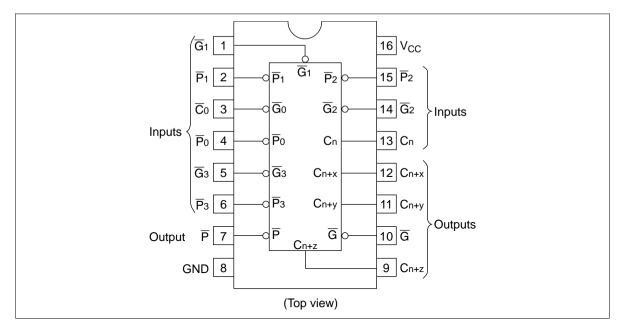
• Low Input Current: 1 μA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

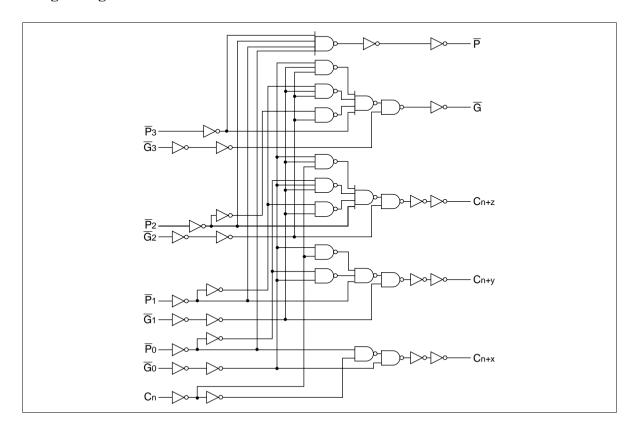
Pin Designations

Item	Pin No.	Functions
$\overline{G_0}, \overline{G_1}, \overline{G_2}, \overline{G_3}$	3, 1, 14, 5	Active-low carry generate inputs
$\overline{\overline{P_0}}, \overline{\overline{P_1}}, \overline{\overline{P_2}}, \overline{\overline{P_3}}$	4, 2, 15, 6	Active-low carry propagate inputs
C _n	13	Carry input
C_{n+x} , C_{n+y} , C_{n+z}	12, 11, 9	Carry outputs
G	10	Active-low carry propagate output
P	7	Active-low carry propagate output
V _{cc}	16	Supply voltage
GND	8	Ground

Pin Arrangement



Logic Diagram



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DC Characteristics

					Ta = −40 to +85°C					
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	าร
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	i —	_	3.15	_	=		
		6.0	4.2	_	_	4.2	_	=		
	V _{IL}	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35			
		6.0	_	_	1.8	_	1.8	=		
Output voltage	V _{OH}	2.0	1.9	2.0		1.9	_	V	Vin = V _{IH} or V _{IL}	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_			
		6.0	5.9	6.0	_	5.9	_	_		
		4.5	4.18	· —		4.13	_			$I_{OH} = -4 \text{ mA}$
		6.0	5.68	i —	_	5.63	_			$I_{OH} = -5.2 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	I _{OL} = 20 μA
		4.5	_	0.0	0.1	_	0.1	_		
		6.0	_	0.0	0.1	_	0.1	_		
		4.5	_	_	0.26	_	0.33	=		I _{OL} = 4 mA
		6.0	_	_	0.26	_	0.33	=		I _{OL} = 5.2 mA
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or GN	ND
Quiescent supply current	I _{cc}	6.0	_	_	4.0	_	40	μA Vin = V_{CC} or GND, lout = $0 \mu A$		

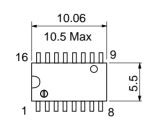
AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

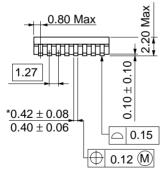
	Ta = -40 to
Ta = 25°C	+85°C

Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	140	_	175	ns	Pn to P
time	$t_{\tiny PHL}$	4.5	_	11	28	_	35	_	
		6.0	_	_	24	_	30	-	
		2.0	_	_	150	_	190	ns	Cn to output
		4.5	_	15	30	_	38	-	
		6.0	_	_	26	_	33	_	
		2.0	_	_	185	_	230	ns	Pn or Gn to output
		4.5	_	17	37	_	46	=	
		6.0	_	_	31	_	39	_	
Output rise/fall	t _{TLH}	2.0	_	_	75	_	95	ns	
time	$t_{\scriptscriptstyle THL}$	4.5	_	5	15	_	19	-	
		6.0	_	_	13	_	16	_	
Input capacitance	Cin	_	_	5	10	_	10	pF	

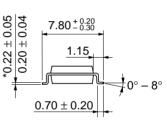
Unit: mm 19.20 20.00 Max 16 7.40 Max 6.30 1.3 1.11 Max 7.62 5.06 Max 2.54 Min 0.51 Min $0.25^{+0.13}_{-0.05}$ 0.48 ± 0.10 2.54 ± 0.25 $0^{\circ} - 15^{\circ}$ Hitachi Code DP-16 **JEDEC** Conforms EIAJ Conforms Weight (reference value) 1.07 g

Unit: mm





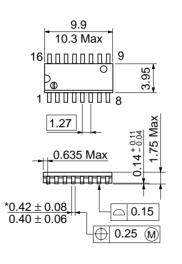


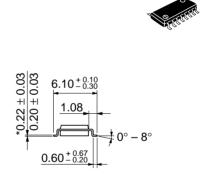


Hitachi Code	FP-16DA
JEDEC	
EIAJ	Conforms
Weight (reference value)	0.24 a

*Dimension including the plating thickness
Base material dimension

Unit: mm





*Dimension including the plating thickness Base material dimension

Hitachi Code	FP-16DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.15 g

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