

2N5308



NPN Darlington Transistor

This device is designed for applications requiring extremely high current gain at currents to 1.0 A. Sourced from Process 05. See MPSA14 for characteristics.

Absolute Maximum Ratings*

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CEO}	Collector-Emitter Voltage	40	V
V _{CBO}	Collector-Base Voltage	40	V
V _{EBO}	Emitter-Base Voltage	12	V
Ic	Collector Current - Continuous	1.2	A
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units	
		2N5308	7	
P _D	Total Device Dissipation Derate above 25°C	625 5.0	mW mW/°C	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	°C/W	

NPN Darlington Transistor (continued)

Symbol	Parameter	Test Conditions	Min	Max	Units
OFF CHA	RACTERISTICS				
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage*	$I_C = 10 \text{ mA}, I_B = 0$	40		V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	$I_C = 0.1 \mu A, I_E = 0$	40		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E = 0.1 \mu A, I_C = 0$	12		V
I _{СВО}	Collector Cutoff Current	V _{CB} = 40 V, I _E = 0 V _{CB} = 40 V, I _E = 0, T _A = 100 °C		0.1 20	μA μA
I _{EBO}	Emitter Cutoff Current	$V_{EB} = 12 \text{ V}, I_{C} = 0$		0.1	μΑ
	RACTERISTICS*	V ₀₌ = 5.0 V I ₀ = 2.0 mA	7 000	70.000	
	DC Current Gain	$V_{CE} = 5.0 \text{ V}, I_{C} = 2.0 \text{ mA}$ $V_{CE} = 5.0 \text{ V}, I_{C} = 100 \text{ mA}$	7,000 20,000	70,000	
h _{FE}		0L , 0	,	70,000	V
h _{FE}	DC Current Gain	$V_{CE} = 5.0 \text{ V}, I_{C} = 100 \text{ mA}$,	-,	V
h_{FE} $V_{CE(sat)}$ $V_{BE(sat)}$	DC Current Gain Collector-Emitter Saturation Voltage	$V_{CE} = 5.0 \text{ V}, I_{C} = 100 \text{ mA}$ $I_{C} = 200 \text{ mA}, I_{B} = 0.2 \text{ mA}$,	1.4	
h _{FE} V _{CE(sat)} V _{BE(sat)} V _{BE(on)}	DC Current Gain Collector-Emitter Saturation Voltage Base-Emitter Saturation Voltage	$V_{CE} = 5.0 \text{ V}, I_{C} = 100 \text{ mA}$ $I_{C} = 200 \text{ mA}, I_{B} = 0.2 \text{ mA}$ $I_{C} = 200 \text{ mA}, I_{B} = 0.2 \text{ mA}$,	1.4 1.6	V
h_{FE} $V_{CE(sat)}$ $V_{BE(sat)}$ $V_{BE(ON)}$	DC Current Gain Collector-Emitter Saturation Voltage Base-Emitter Saturation Voltage Base-Emitter On Voltage	$V_{CE} = 5.0 \text{ V}, I_{C} = 100 \text{ mA}$ $I_{C} = 200 \text{ mA}, I_{B} = 0.2 \text{ mA}$ $I_{C} = 200 \text{ mA}, I_{B} = 0.2 \text{ mA}$,	1.4 1.6	V
$\begin{array}{c} h_{\text{FE}} \\ V_{\text{CE(sat)}} \\ V_{\text{BE(sat)}} \\ V_{\text{BE(on)}} \end{array}$	DC Current Gain Collector-Emitter Saturation Voltage Base-Emitter Saturation Voltage Base-Emitter On Voltage GNAL CHARACTERISTICS	$V_{CE} = 5.0 \text{ V}, I_{C} = 100 \text{ mA}$ $I_{C} = 200 \text{ mA}, I_{B} = 0.2 \text{ mA}$ $I_{C} = 200 \text{ mA}, I_{B} = 0.2 \text{ mA}$ $I_{C} = 200 \text{ mA}, V_{CE} = 5.0 \text{ V}$,	1.4 1.6 1.5	V

^{*}Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%