

# **DCR1574SY**

# **Phase Control Thyristor**

Replaces January 2000 version, DS4400-3.0

DS4400-4.0 July 2001

### **FEATURES**

- Double Side Cooling.
- High Surge Capability.
- High Current.
- Fatigue Free.

### **APPLICATIONS**

- High Power Drives.
- High Voltage Power Supplies.
- DC Motor Control.

### **VOLTAGE RATINGS**

Type Number	Repetitive Peak Voltages V <sub>DRM</sub> V	Conditions
DCR1574SY28 DCR1574SY27 DCR1574SY26 DCR1574SY25 DCR1574SY24	2800 2700 2600 2500 2400	$\begin{split} & T_{vj} = 0^{\circ} \text{ to } 125^{\circ}\text{C}, \\ & I_{DRM} = I_{RRM} = 300\text{mA}, \\ & V_{DRM}, V_{RRM} t_{p} = 10\text{ms}, \\ & V_{DSM} \& V_{RSM} = \\ & V_{DRM} \& V_{RRM} + 100V \\ & \text{respectively} \end{split}$

Lower voltage grades available.

#### **ORDERING INFORMATION**

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

#### DCR1574SY25

Note: Please use the complete part number when ordering and quote this number in any future correspondance relating to your order.

### **KEY PARAMETERS**

 $V_{DRM}$  2800V  $I_{T(AV)}$  3419A  $I_{TSM}$  54500A dVdt 1000V/ $\mu$ s dI/dt 300A/ $\mu$ s

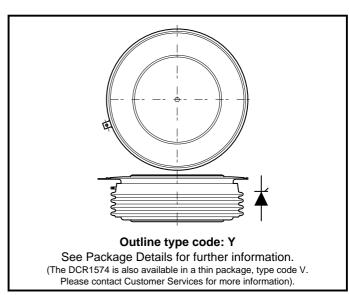


Fig. 1 Package outline



### **CURRENT RATINGS**

T<sub>case</sub> = 60°C unless stated otherwise

Symbol	Parameter	Conditions	Max.	Units		
Double Sid	Double Side Cooled					
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	3419	А		
I <sub>T(RMS)</sub>	RMS value	-	5370	А		
Ι <sub>τ</sub>	Continuous (direct) on-state current	-	4836	А		
Single Side Cooled (Anode side)						
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	2197	А		
I <sub>T(RMS)</sub>	RMS value	-	3451	Α		
I <sub>T</sub>	Continuous (direct) on-state current	-	2857	А		

# **CURRENT RATINGS**

 $T_{\text{case}} = 80^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Conditions	Max.	Units			
Double Sic	Double Side Cooled						
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	2667	А			
I <sub>T(RMS)</sub>	RMS value	-	4189	А			
I <sub>T</sub>	Continuous (direct) on-state current	-	3680	А			
Single Side	Single Side Cooled (Anode side)						
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	1680	А			
I <sub>T(RMS)</sub>	RMS value	-	2640	Α			
I <sub>T</sub>	Continuous (direct) on-state current	-	2140	А			



# **SURGE RATINGS**

Symbol	Parameter	Conditions	Max.	Units
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; T <sub>case</sub> = 125°C	43.8	kA
l <sup>2</sup> t	I <sup>2</sup> t for fusing	V <sub>R</sub> = 50% V <sub>RRM</sub> - 1/4 sine	9.59 x 10 <sup>6</sup>	A²s
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; T <sub>case</sub> = 125°C	54.5	kA
l <sup>2</sup> t	I <sup>2</sup> t for fusing	$V_R = 0$	14.85 x 10 <sup>6</sup>	A²s

# THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions		Min.	Max.	Units
	Thermal resistance - junction to case	Double side cooled	dc	-	0.0095	°C/W
R <sub>th(j-c)</sub>		Single side cooled	Anode dc	-	0.019	°C/W
			Cathode dc	-	0.019	°C/W
R <sub>th(c-h)</sub>	Thermal resistance - case to heatsink	Clamping force 50.0kN with mounting compound	Double side	-	0.002	°C/W
			Single side	-	0.004	°C/W
T <sub>vj</sub>	Virtual junction temperature	On-state (conducting)		-	135	°C
		Reverse (blocking)		-	125	°C
T <sub>stg</sub>	Storage temperature range			-55	150	°C
-	Clamping force			45.0	55.0	kN



# **DYNAMIC CHARACTERISTICS**

Symbol	Parameter	Conditions		Тур.	Max.	Units
I <sub>RRM</sub> /I <sub>DRM</sub>	Peak reverse and off-state current	At V <sub>RRM</sub> /V <sub>DRM</sub> , T <sub>case</sub> = 125°C		-	300	mA
dV/dt	Maximum linear rate of rise of off-state voltage	To 67% V <sub>DRM</sub> T <sub>j</sub> = 125°C.		-	1000	V/μs
-11 / -14		From 80% V <sub>DRM</sub>	Repetitive 50Hz	-	250	A/μs
dl/dt	Rate of rise of on-state current	Gate source 20V, $10\Omega$ t <sub>r</sub> < 0.5 $\mu$ s. To JEDEC RS397 N	Non-repetitive	-	500	A/μs
V <sub>T(TO)</sub>	Threshold voltage	At T <sub>vj</sub> = 125°C		-	0.883	V
r <sub>T</sub>	On-state slope resistance	At T <sub>vj</sub> = 125°C		-	0.11	mΩ
t <sub>gd</sub>	Delay time	$V_D$ = 67% $V_{DRM}$ , Gate source 30V, 15Ω Rise time 0.5μs, $T_j$ = 25°C		-	2	μs
I <sub>L</sub>	Latching current	$T_{j} = 25^{\circ}C, V_{D} = 5V$		-	350	mA
I <sub>H</sub>	Holding current	$T_j = 25^{\circ}C$ , $R_{g-k} = \infty$		-	100	mA
t <sub>q</sub>	Turn-off time	$I_T = 400A$ , $t_p = 1ms$ , $T_j = 125$ °C, $V_{RM} = 50V$ , $dI_{RR}/dt = 40A/\mu s$ , $V_{DR} = 900V$ , $dV_{DR}/dt = 40V/\mu s$ linear		400	-	μs

### **GATE TRIGGER CHARACTERISTICS AND RATINGS**

Symbol	Parameter	Conditions	Max.	Units
$V_{GT}$	Gate trigger voltage	$V_{DRM} = 5V$ , $T_{case} = 25^{\circ}C$	3.0	V
I <sub>GT</sub>	Gate trigger current	$V_{DRM} = 5V$ , $T_{case} = 25^{\circ}C$	300	mA
$V_{GD}$	Gate non-trigger voltage	At V <sub>DRM</sub> T <sub>case</sub> = 125°C	0.25	V
V <sub>FGM</sub>	Peak forward gate voltage	Anode positive with respect to cathode	30	V
V <sub>FGN</sub>	Peak forward gate voltage	Anode negative with respect to cathode	0.25	V
V <sub>RGM</sub>	Peak reverse gate voltage		5	V
I <sub>FGM</sub>	Peak forward gate current	Anode positive with respect to cathode	30	А
P <sub>GM</sub>	Peak gate power	See figs. 7 and 8, gate characteristics table	150	W
P <sub>G(AV)</sub>	Mean gate power		10	W



### **CURVES**

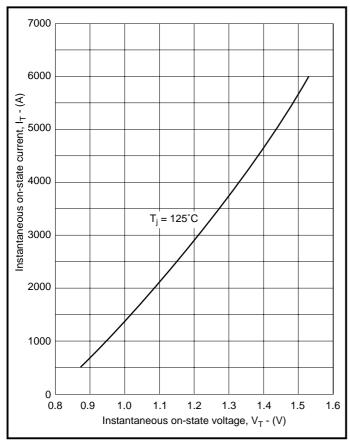


Fig.2 Maximum (limit) on-state characteristics



$$V_{TM} = A + BIn (I_T) + C.I_T + D. \sqrt{I_T}$$

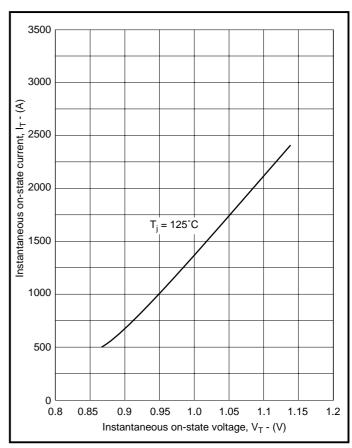


Fig.3 Maximum (limit) on-state characteristics

Where A = 1.328994

B = -0.1381631

 $C = 3.565973 \times 10^{-6}$ 

D = 0.01786171

These values are valid for  $T_i = 125^{\circ}C$  for  $I_T 500A$  to 6000A



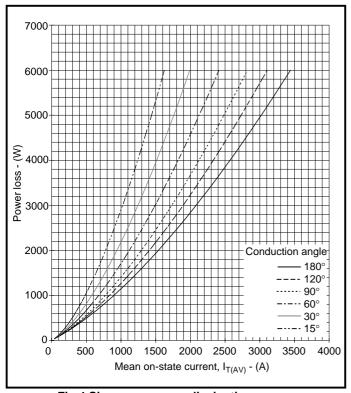


Fig.4 Sine wave power dissipation curves

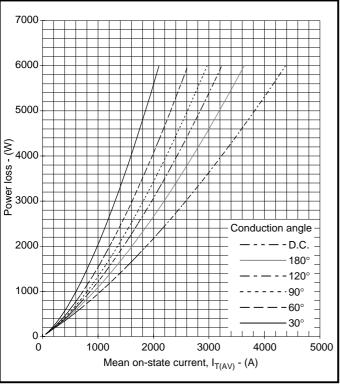


Fig.6 Square wave power dissipation curves

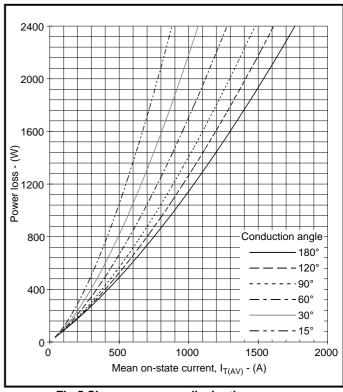


Fig.5 Sine wave power dissipation curves

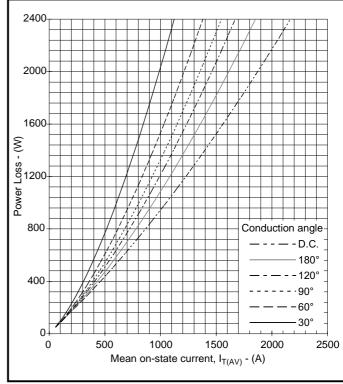


Fig.7 Square wave power dissipation curves



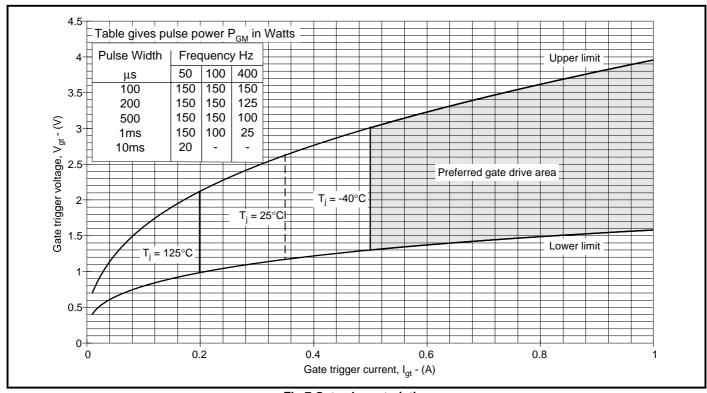


Fig.7 Gate characteristics

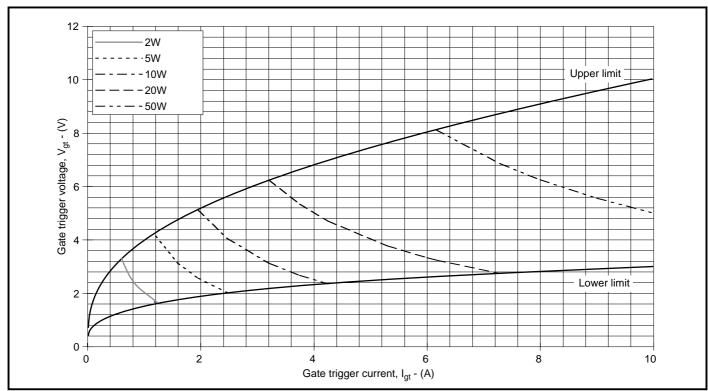
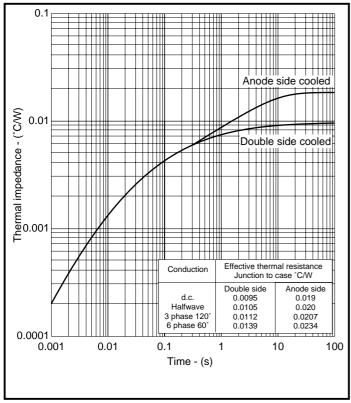
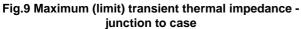


Fig.8 Gate characteristics







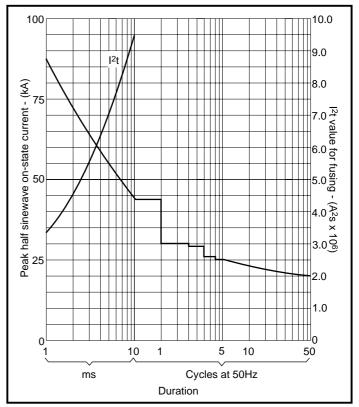
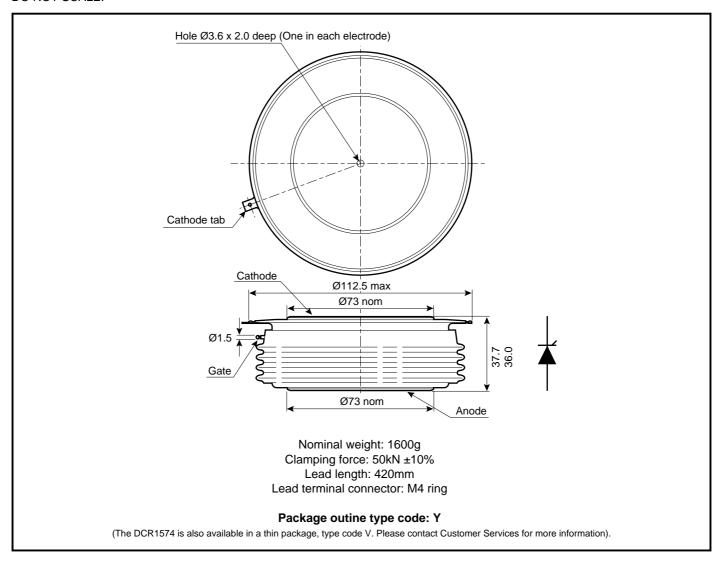


Fig.10 Surge (non-repetitive on-state current vs time



### **PACKAGE DETAILS**

For further package information, please contact your nearest Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





### **POWER ASSEMBLY CAPABILITY**

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

#### **DEVICE CLAMPS**

Disc devices require the correct clamping force to ensure their safe operation. The PACS range includes a varied selection of pre-loaded clamps to suit all of our manufactured devices. Types available include cube clamps for single side cooling of 'T' 23mm and 'E' 30mm discs, and bar clamps right up to 83kN for our 'Z' 100mm thyristors and diodes.

Clamps are available for single or double side cooling, with high insulation versions for high voltage assemblies.

Please refer to our application note on device clamping, AN4839

#### **HEATSINKS**

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or customer service office.



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Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

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